

SITE CLASSIFICATION	
Class	Subclass

JL 1061

## U.S. UTILITY PATENT APPLICATION

KC O.I.P.E.  
 SCANNED 1/10/04 O.A. 2/10/04

PATENT NUMBER

PATENT DATE

APPLICATION NO. 09/960399	CONT/PRIOR F	CLASS 257	SUBCLASS	ART UNIT 2841	EXAMINER C. J. ...
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Koji Takahashi

## APPLICANTS

**TITLE**

Semiconductor integrated circuit and fabrication process thereof

PTO-2040  
12/89

**PREPARED AND APPROVED FOR ISSUE**

**ISSUING CLASSIFICATION**

<b>ISSUING CLASSIFICATION</b>							
<b>ORIGINAL</b>				<b>CROSS REFERENCE(S)</b>			
<b>CLASS</b>		<b>SUBCLASS</b>	<b>CLASS</b>	<b>SUBCLASS (ONE SUBCLASS PER BLOCK)</b>			
<b>INTERNATIONAL CLASSIFICATION</b>							
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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> e) The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  <input type="checkbox"/> c) The terminal _____ months of this patent have been disclaimed.	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner)		<b>ISSUE FEE</b>	
			Amount Due	Date Paid
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